**Meeting Summary**

**~~1. 所有BUS数字型号输入 Header后面要并联一个1M欧姆的电阻。做静电保护~~**

* ~~Add a 1MΩ resistor in parallel behind each header to provide electrostatic protection.~~

**~~2. Necleo板子的Pin config表格需要制作。~~**

* ~~Create and refine a table for the configuration of all pins on the Nucleo board.~~

**~~3. RTOS任务优先级架构图~~**

* ~~Draw an RTOS task priority architecture diagram to present a clearer structure in the report.~~

**4. 完成以上图纸与表格制作后，完善Report。**

* Update and finalize the project report, ensuring that all technical details and architecture are clearly documented.

**5. 学习Presentation的模板与格式，周六制作出草稿**

* Study the presentation template and format, and prepare a draft by Saturday.

**杂项 / Miscellaneous**

**~~1. DAC 的 Vref 和 VDD 走3.3V 其他也默认3.3V吧~~**

* ~~Set both Vref and VDD to 3.3V, with other voltages also defaulting to 3.3V.~~

**~~2. DAC 的 CS 芯片 的OE和MR 需要修改 保证处在 "Out Consnetly enabled"~~**

* ~~Modify the OE and MR configurations of the DAC chip to ensure it remains continuously enabled for output.~~

**3. 参考Nuclo板子3.3V的电学稳定性 考虑是否需要独立设计Vref电路**

* Assess whether a separate Vref circuit design is necessary to enhance voltage stability based on the 3.3V electrical stability of the Nucleo board.

**后续事项 / Next Steps**

**1. presentation 后schedule下一个会议**

* Schedule the next meeting after the presentation.

**2. ~~周五前交不上的话 明天12前发邮件，~~**

* ~~If submission cannot be completed by Friday, send an email by noon tomorrow to explain the situation.~~

**周一下午五点前交差 周日定稿**